INTERFERRENCE

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L26	0	((memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3 and configur$4 and logic$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:43

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	29444	memory same attribut$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:25
L2	4911	(memory same attribut$3) and configur$3 and logical	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:27
L3	2543	(memory same attribut$3) and configur$3 and logical and interconnect$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:27
L4	29666	FPGA or (programmable adj logic adj design) same implement$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:29
L5	314	(FPGA or (programmable adj logic adj design) same implement$5) and (memory same attribut$3) and configur$3 and logical and interconnect$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:30
L6	101	(FPGA or (programmable adj logic adj design) same implement$5) and (memory same attribut$3) and configur$3 and logical and interconnect$3 and topology	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:30
L7	14	(FPGA or (programmable adj logic adj design) same implement$5) and (memory same attribut$3) and configur$3 and logical and interconnect$3 and topology and "716"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:30
L8	14	(FPGA or (programmable adj logic adj design) same implement$5) and (memory same attribut$3) and configur$3 and logical and interconnect$3 and topology and component and "716"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:31

L9	91	(FPGA or (programmable adj logic adj design) same implement$5) and (memory same attribut$3) and configur$3 and logical and interconnect$3 and topology and component and architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:32
L10	101	(FPGA or CLB or PLD or (programmable adj logic adj design)) and (memory same attribut$3) and configur$3 and logical and interconnect$3 and topology and component and architecture and implement$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:34
L12	2481	memory same component same attribut$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:36
L14	1801	(memory same component same attribut$3) and interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:37
L16	975	(memory same component same attribut$3) and interface and implement$6 and specification	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:38
L19	226	(memory same component same attribut$3) and interface and implement$6 and specification and topology	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:39
L20	198	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:39
L21	177	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:39

L22	177	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:40
L23	167	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3 and configur$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:41
L24	2	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3 and configur$4 and "716"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:42
L25	165	(memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3 and configur$4 and logic$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:43
L26	0	((memory same component same attribut$3) and interface and implement$6 and specification and topology and interconnect$4 and messag$3 and generat$3 and configur$4 and logic$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 14:43